

ABSTRACT

There is disclosed visual artifact reduction methods for a display comprising the use of gamma corrections, error diffusion, dithering, and/or center of light. The invention is described with reference to an AC gas discharge display (PDP), but may be practiced with other display technologies. The methods of this invention are disclosed for use with a number of PDP structures and PDP electronic addressing architectures including ADS, SAS, and ALIS. In one preferred embodiment, a center of light timing method is used to reduce artifacts between different sections of a PDP being addressed with SAS architecture.